



ISLAMIC UNIVERSITY OF SCIENCE & TECHNOLOGY AWANTIPORA, KASHMIR

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Design Innovation Centre

Notice

This is for the information of all the 3rd and 4th year Electrical Engineering, Electronics & Communication Engineering and Computer Science Engineering students of IUST, who have registered themselves for the Certificate Course on 'PCB Design Learning on Altium Designer' that their final selection will be based on a screening test, to be conducted as per the following schedule:

Date of screening test: 01-11-2017 (Wednesday)

Time: 01: 30 PM.

Venue: Auditorium, 1st Floor, Academic Block III, IUST

Entrance test Syllabus: An objective test based on basic aptitude and knowledge of Basic Electric Circuits and Basic Electronics.

Note: Students are advised to be at the venue, 15 minutes prior to the scheduled time of screening test.

Mr. Peerzada Shoaib Hamid
(Coordinator, SOFTEND Club)

