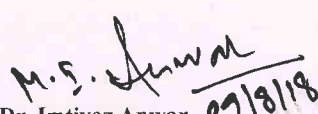


DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING, IUST AWANTIPORA


Date sheet of Supplementary Examination for B. Tech 3rd & 5th semester

TIMING: 10:00 a.m. – 12:30 p.m.

SEM	DATE / DAY	COURSE TITLE (COURSE CODE)	VENUE
3 RD	14-08-2018/ TUESDAY	Digital Elx. and Logic Design (ECE-313T) Electromagnetic Fields and Waves (ECE-314T)	ABVI-101
	16-08-2018/ THURSDAY	Analog Electronics-I (ECE-311T)	
	17-08-2018/ FRIDAY	Network Analysis (ECE-312T)	
	20-08-2018/ MONDAY	Laplace, Fourier & Complex Analysis (MTH-313T) Elements of Digital Logic-(Elective-I -Generic)	
5 TH	14-08-2018/ TUESDAY	Microprocessors (ECE-512T)	ABVI-101
	16-08-2018/ THURSDAY	Electronic Instrumentation and Measurement (ECE-511T) Electronic Engg Materials & Device fabrication (ECE-513T)	
	17-08-2018/ FRIDAY	Antenna, Wave Propagation & Transmission Lines (ECE-514T)	
	20-08-2018/ MONDAY	Electrical Machines - II (ELE-518T) Mobile Communication (ECE-E12T) VLSI Design (ECE-E09T)	


Dr. Imtiyaz Anwar
Asstt. Coordinator Exams, ECE


Mr. Zahid G Khaki
Coordinator Exams, ECE


Prof. A. H. Moon
Head, E&C Engg

No:IUST/ECE/DATESHEET/18/195
Dated:09-08-2018

Copy to:

1. Dean SoET
2. Head, EE/ME/CSE/CE/CS/FT/Business Studies
3. I/C Controller Exams with the request that the link for submission of supplementary forms may be made available
4. Deputy Registrar Academics
5. University website
6. Notice Board
7. File