

Department of Computer Science and Engineering

DATE SHEET FOR END TERM EXAMINATION (REREGISTRATION/ BACKLOG)
B.TECH CSE 1st, 3rd & 5th SEMESTER (Autumn 2015)

Date/Day	Timing	Course Name (Course Code)	Venue
01-01-2016/ Friday	10:15 a.m.- 12:45 p.m.	Electrical Measurement (CSE-304T)/ Mathematics –I (CSE-103T) /Electric Circuits(CSE-102T)	Programming lab CSE New Building
04-01-2016/ Monday	10:15 a.m.- 12:45 p.m.	Object Oriented Programming and System Design (CSE-501T)/ Mathematics- III (CSE-305T)/	
05-01-2016/ Tuesday	10:15 a.m.- 12:45 p.m.	Mathematics –V (CSE- 505T)/ Network Analysis (CSE-302T)/ Digital Electronics and Logic Design (CSE- 303T)/	
06-01-2016/ Wednesday	10:15 a.m.- 12:45 p.m.	Algorithm Analysis and design (CSE-504T)/ Basic Electrical Engineering(CSE-306T)/	
07-01-2016/ Thursday	10:15 a.m.- 12:45 p.m.	Electronics-II (CSE-301T)/ Microprocessors Peripherals & Interfacing (CSE-506T)	
	01:00p.m. – 03:30p.m.	Data Structures (CSE-404T)	
08-01-2016/ Friday	10:15 a.m.- 12:45 p.m.	Electronics Engineering Materials (CSE 303T)/ DBMS (CSE-302T)	

Ms. Saima Maqbool
Asst. I/C Exams CSE

Mr. Sajjad Ahmad Lone
I/C Examination CSE

Prof. A.G. Lone
H.O.D CSE

Copy to:

1. Dean SOT
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6. Notice Board/File

No: IUST/CSE/Date Sheet/

Dated: 15/12/2015